

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,197,625 B1
APPLICATION NO. : 09/662832
DATED : March 27, 2007
INVENTOR(S) : Van Hook et al.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Item [63], Related U.S. Application Data section please replace "5,266,738" with
--5,266,758--.

Item [63], Related U.S. Application Data section please replace "5,933,630" with
--5,933,650--.

Title page illustrating a fig. should be deleted, and replaced with new title page
illustrating a fig. (attached).

Drawings

Delete Drawing Sheets 1-17, and replace with Drawing Sheets 1-17. (attached)

Column 16

Line 3 replace "(SIMS)" with --(SIMD)--.

Line 11 replace "data byte and" with --data bytes and--.

Line 19 replace "comprising" with --comprising: --.

Line 33 replace "avector" with --a vector--.

Line 39 replace "unit." with --unit--.

Line 44 replace "writers" with --writes--.

Column 17

Line 23 replace "specified specified" with --specified--.

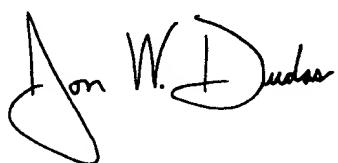
Column 18

Line 11 replace "data of" with --data elements of--.

Line 22 replace "ordering mod." with --ordering mode--.

Signed and Sealed this

Eleventh Day of December, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office

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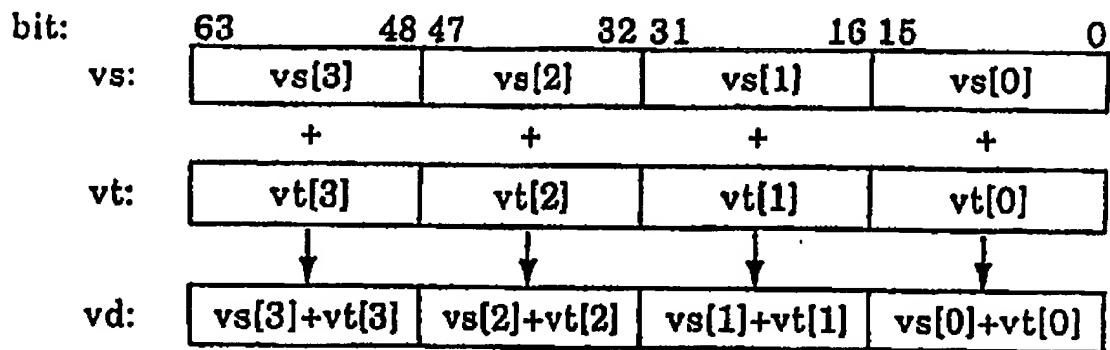


FIG. 1
(Prior Art)

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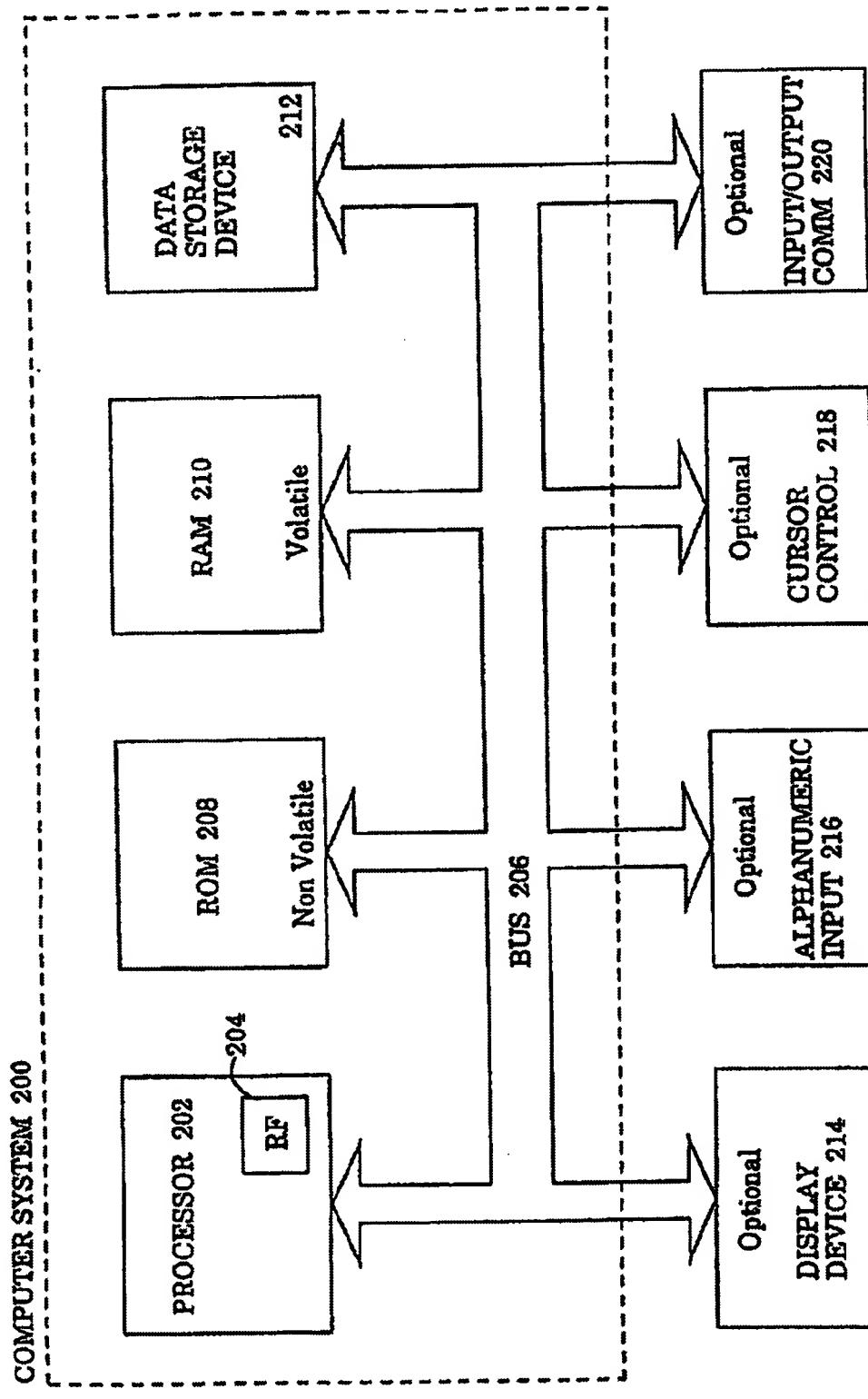


FIG. 2 (Prior Art)

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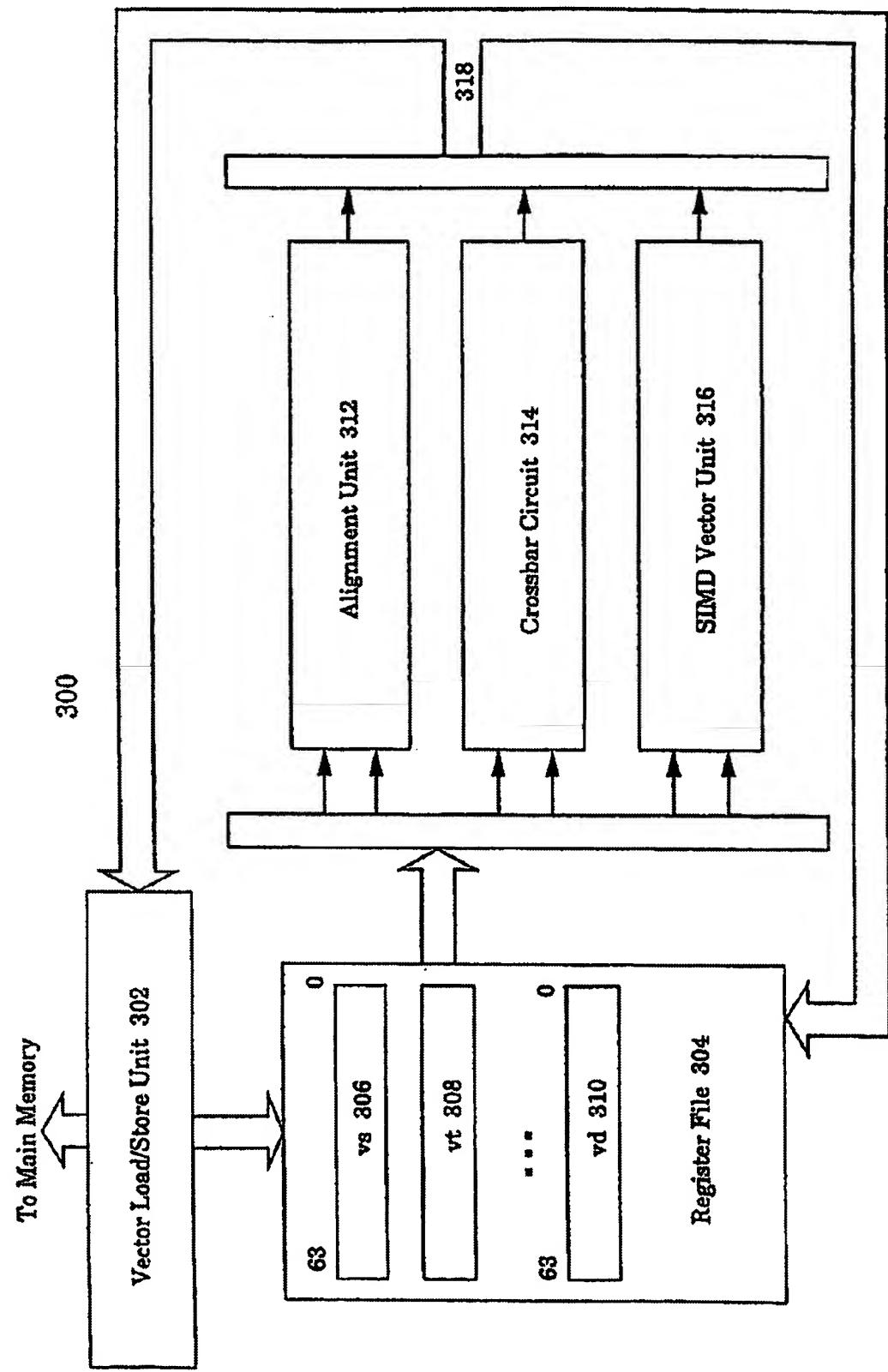


FIG. 3

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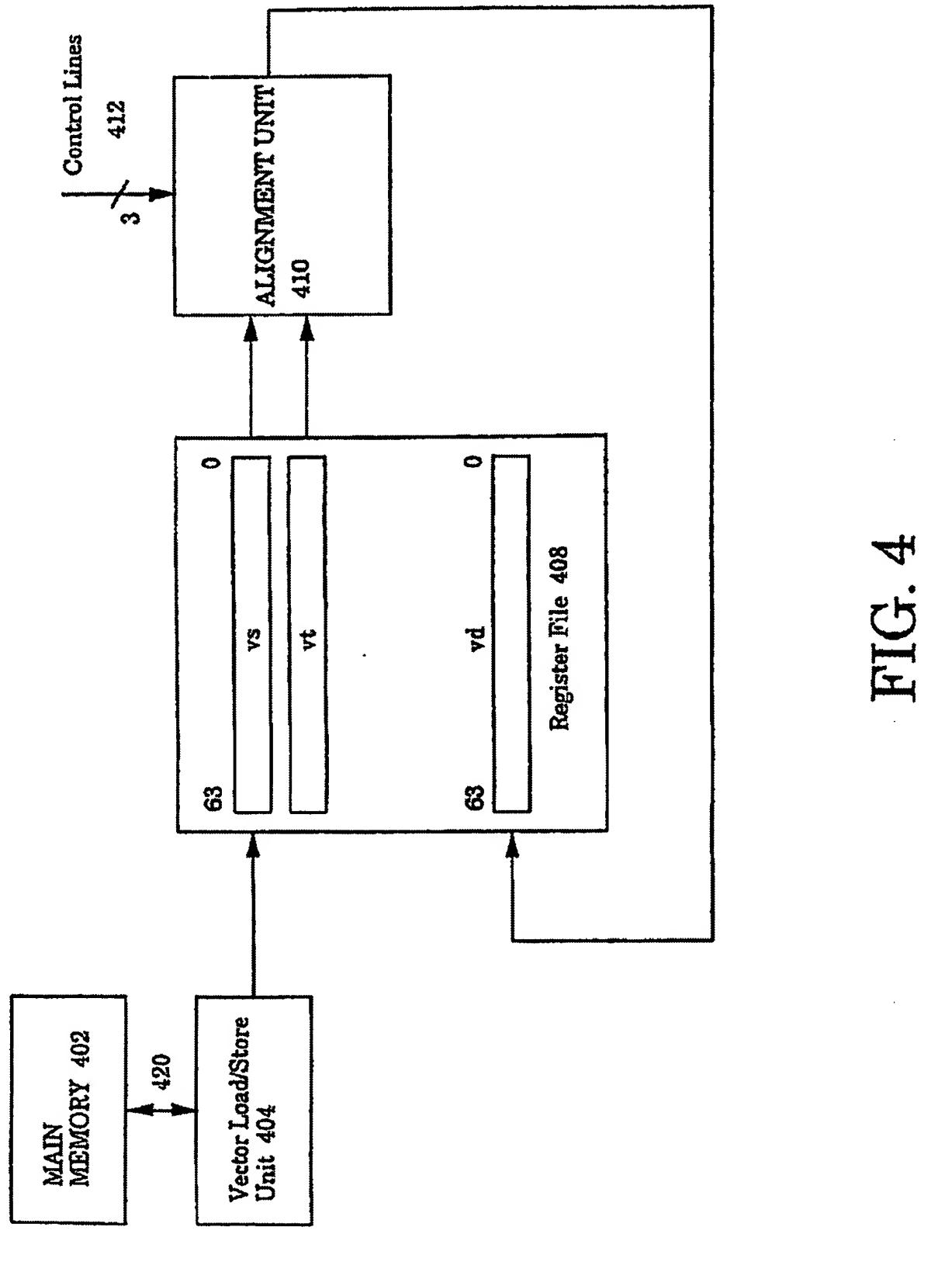


FIG. 4

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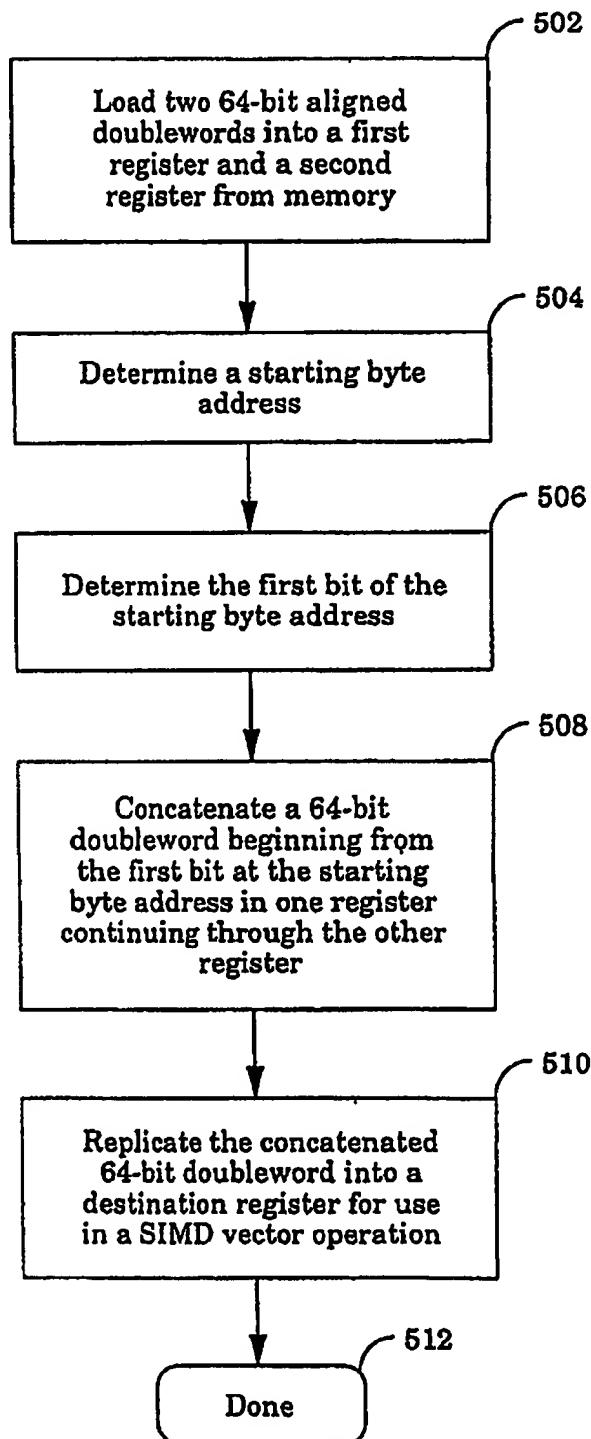


FIG. 5

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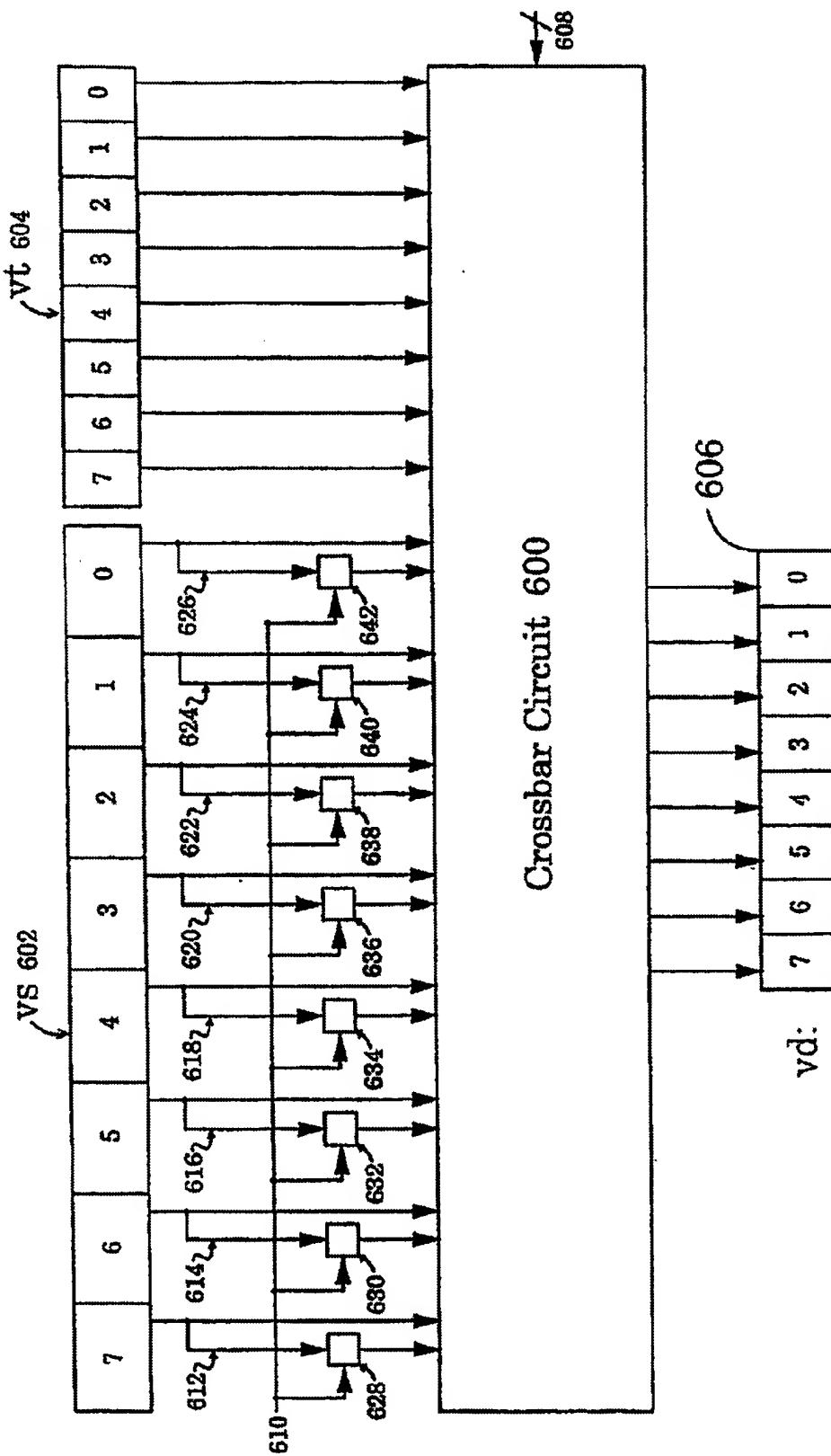


FIG. 6A

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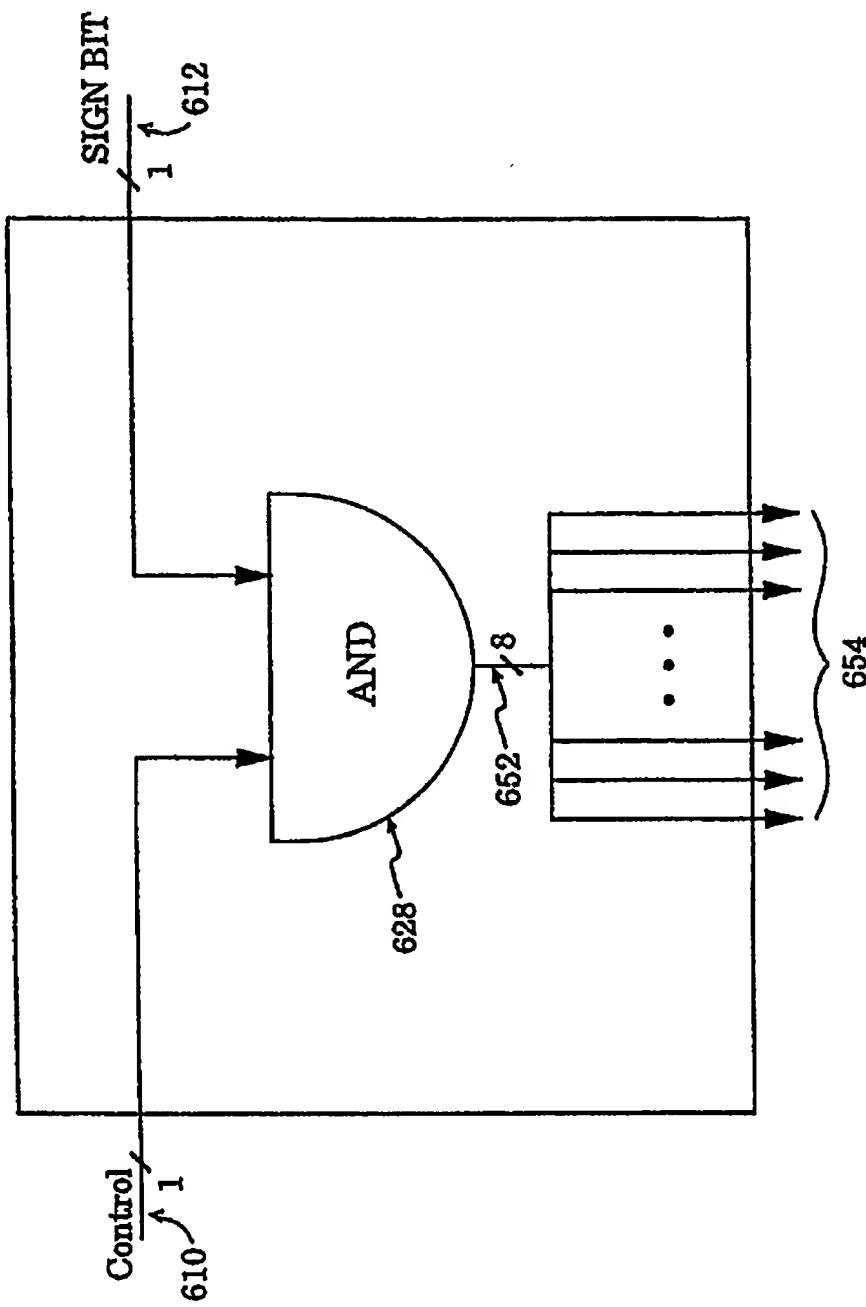


FIG. 6B

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63	vd[7]	vd[6]	vd[5]	vd[4]	vd[3]	vd[2]	vd[1]	vd[0]	702
0	vs[7]	0	vs[6]	0	vs[5]	0	vs[4]	vs[3]	704
0	vs[3]	0	vs[2]	0	vs[1]	0	vs[0]	vs[1]	706
sign vs[7]	vs[7]	sign vs[6]	vs[6]	sign vs[5]	vs[5]	sign vs[4]	vs[4]	vs[3]	708
sign vs[3]	vs[3]	sign vs[2]	vs[2]	sign vs[1]	vs[1]	sign vs[0]	vs[0]	vs[1]	710
vs[7]	vs[5]	vs[3]	vs[1]	vt[7]	vt[5]	vt[3]	vt[1]	vt[2]	712
vs[6]	vs[4]	vs[2]	vs[0]	vt[6]	vt[4]	vt[2]	vt[0]	vt[1]	714
vs[7]	vt[7]	vs[6]	vt[6]	vs[5]	vt[5]	vs[4]	vt[4]	vt[3]	716
vs[3]	vt[3]	vs[2]	vt[2]	vs[1]	vt[1]	vs[0]	vt[0]	vt[2]	718

FIG. 7

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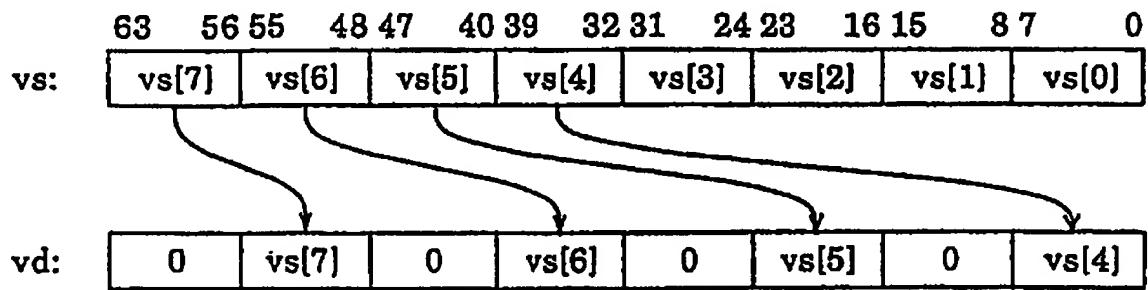
UPUH

FIG. 8A

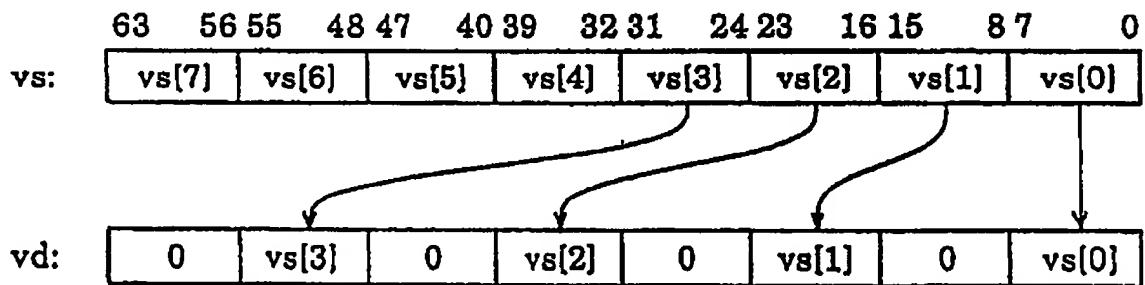
UPUL

FIG. 8B

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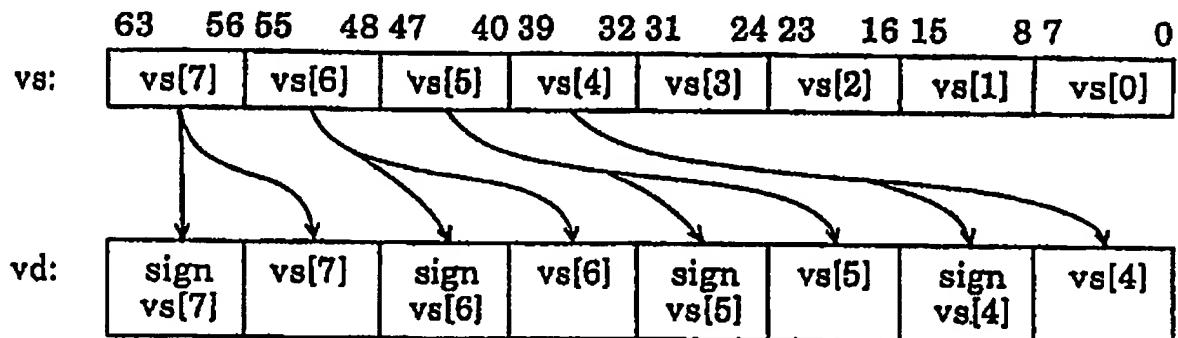
UPSH

FIG. 8C

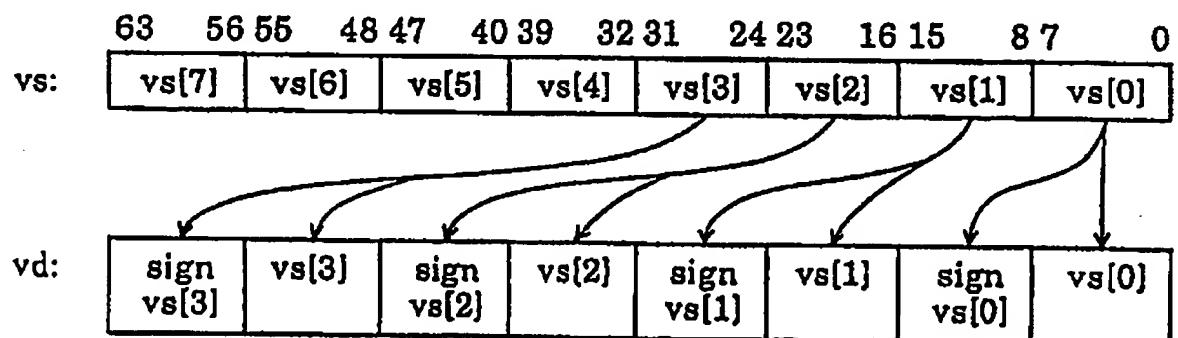
UPSL

FIG. 8D

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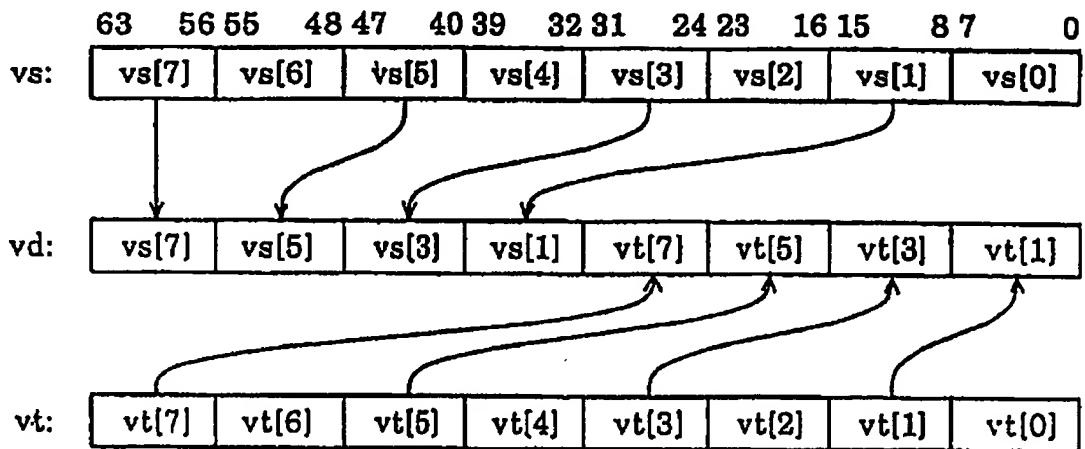
PACH

FIG. 8E

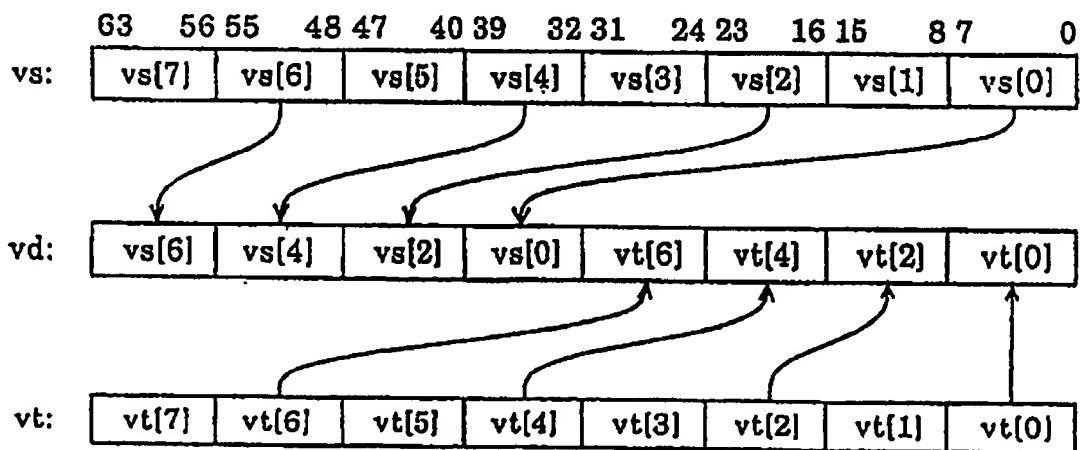
PACL

FIG. 8F

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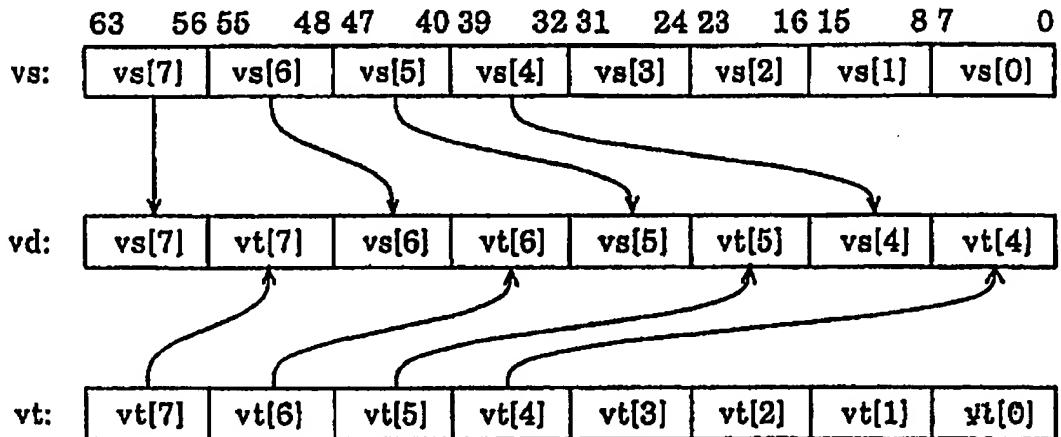
MIXH

FIG. 8G

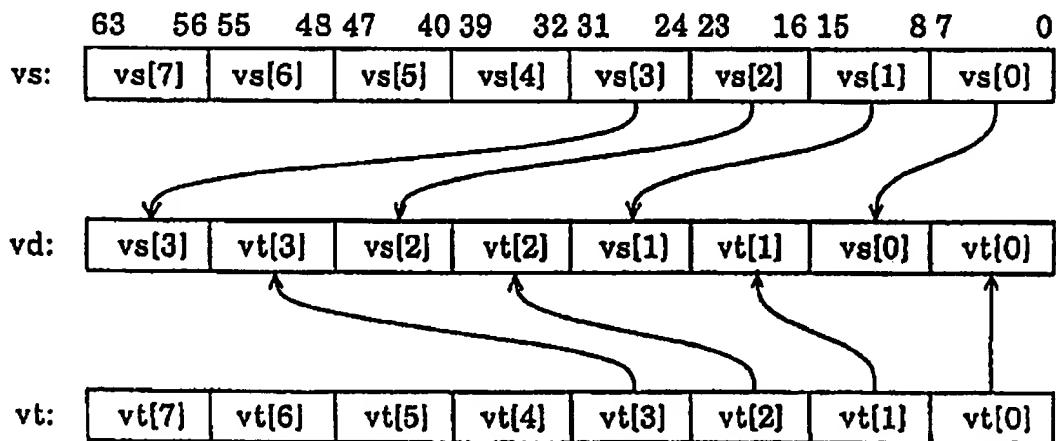
MIXL

FIG. 8H

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63	0	
vd[3]	vd[2]	vd[1]
vs[3]	vt[3]	vs[2]
vs[1]	vt[1]	vs[0]
vs[3]	vs[1]	vt[3]
vs[2]	vs[0]	vt[2]
vs[2]	vt[3]	vs[0]
vs[0]	vt[1]	vs[2]
vs[3]	vs[2]	vt[3]
vs[1]	vs[0]	vt[1]

902
904
906
908
910
912
914
916
918

FIG. 9

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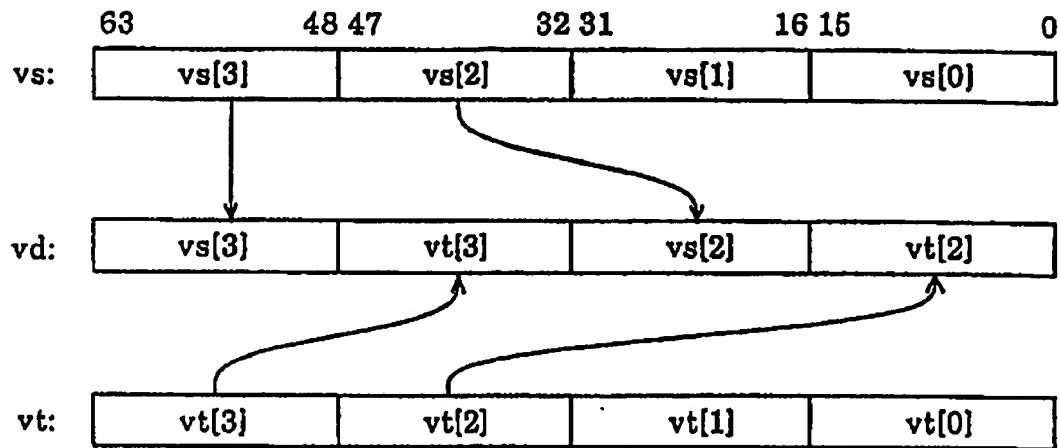
MIXH

FIG. 10A

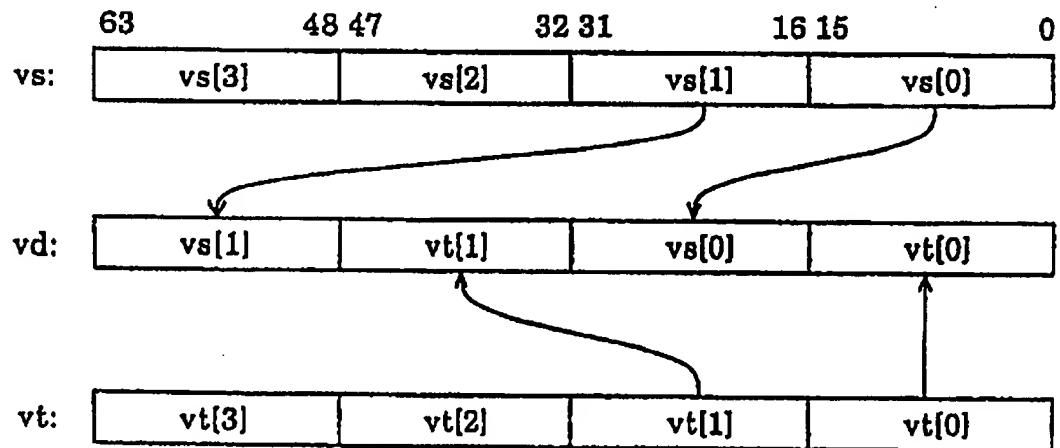
MIXL

FIG. 10B

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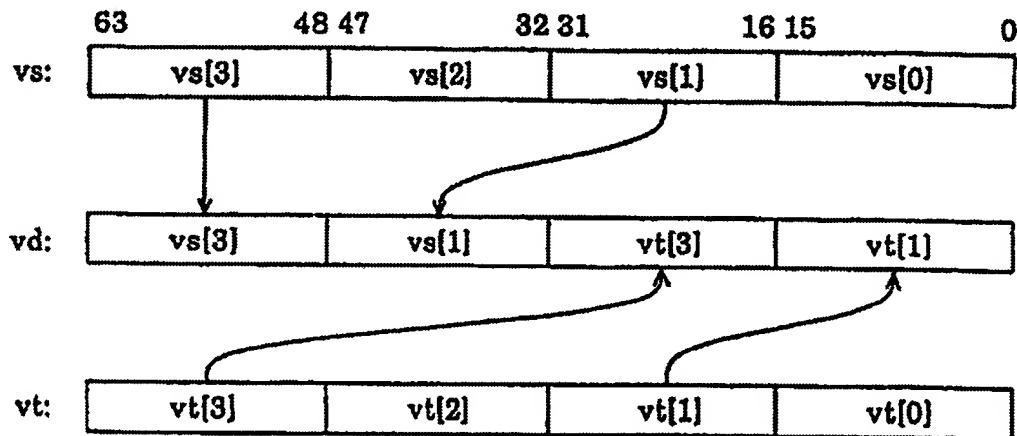
PACH

FIG. 10C

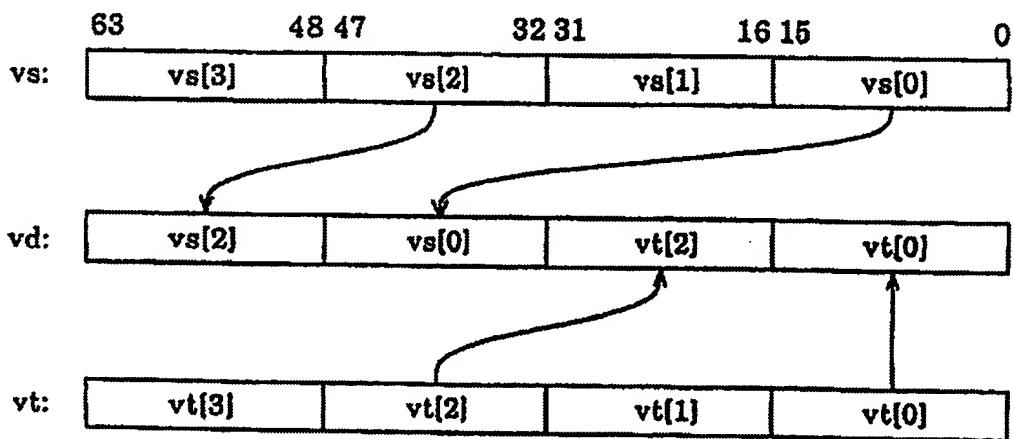
PACL

FIG. 10D

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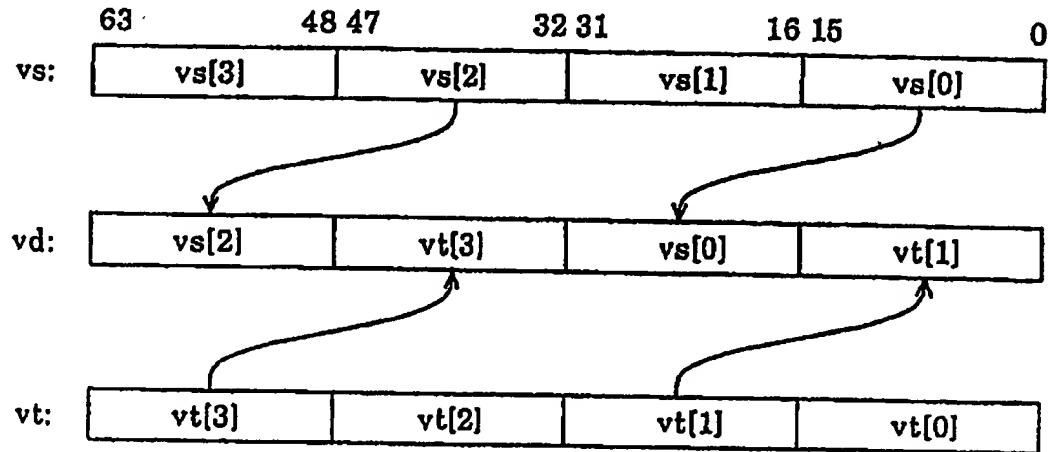
BFLA

FIG. 10E

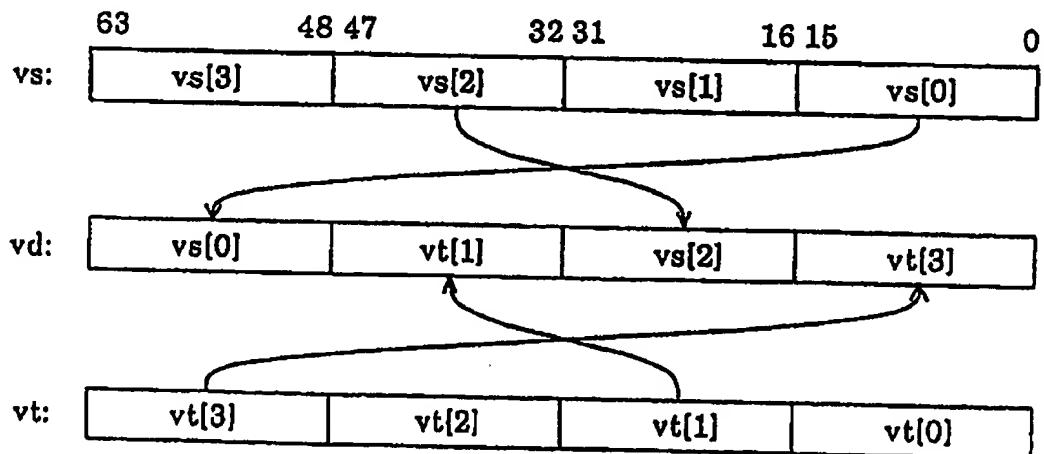
BFLB

FIG. 10F

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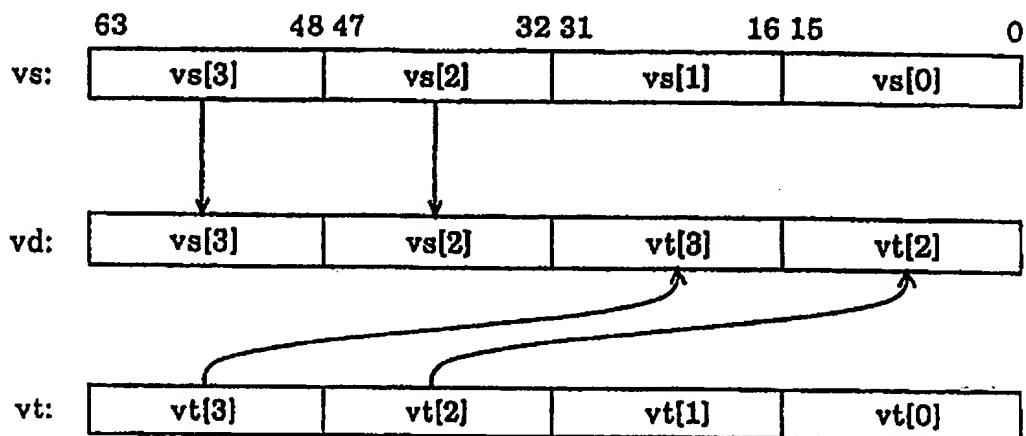
REPA

FIG. 10G

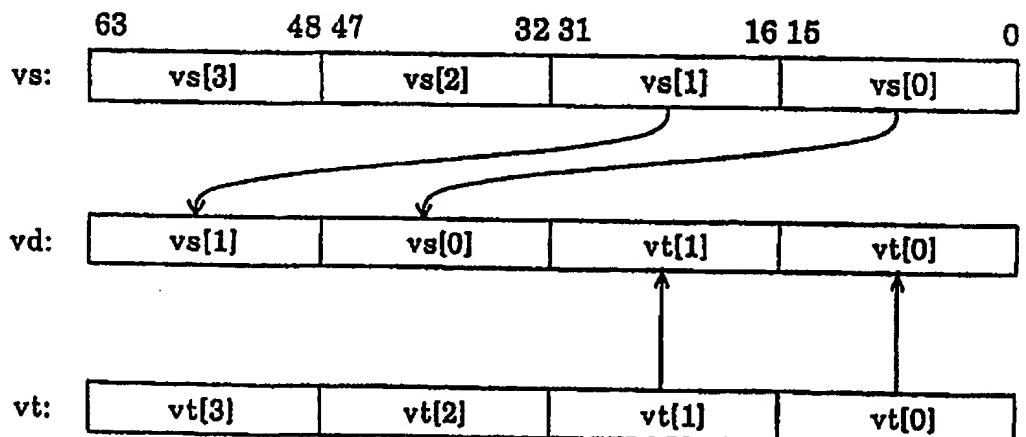
REPB

FIG. 10H